

Notice of Allowability

Application No.

10/718,674

Examiner

Thomas L. Dickey

Applicant(s)

CHANG, MING-HSUAN

Art Unit

2826

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to amendment filed 06/14/2005.
2. ☒ The allowed claim(s) is/are 10,11 and 13-15.
3. ☒ The drawings filed on 24 November 2003 are accepted by the Examiner.
4. ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some* c) ☐ None of the:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).
- * Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.

THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

5. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
6. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
- (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
- 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
- (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.
- Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
7. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/08), Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material
5. ☐ Notice of Informal Patent Application (PTO-152)
6. ☐ Interview Summary (PTO-413), Paper No./Mail Date _____
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____

Minhloan Tran
Minhloan Tran
Primary Examiner
Art Unit 2826

Art Unit: 2826

REASONS FOR ALLOWANCE

1. The following is an examiner's statement of reasons for allowance:

Claims 10,11 and 13-15 are allowed over the references of record because none of these references disclosed or can be combined to yield the claimed invention such as a transistor having all the limitations of claim 10 as previously rejected, and in addition, the limitation that said projection of said fourth conducting structure onto said substrate approximately parallel to said projection of said third conducting structure onto said substrate at least seven times longer than said side of said projection of said fourth conducting structure onto said substrate approximately parallel to said projection of said first conducting structure onto said substrate.

Morozumi 4,600,274 discloses a transistor with a first conducting structure 43, for example, a signal/data line, upon a substrate 40; a second conducting structure 44, for example a scanning/gate line, upon said substrate 40, with the projection of said second conducting structure (scanning/gate line 44) onto said substrate 40 intersecting the projection of said first conducting structure (signal/data line 43) onto said substrate 40; a third conducting structure 53, for example a source, upon said substrate 40 contacting with said first conducting structure (signal/data line 43), with the projection of said third conducting structure (source 53) onto said substrate 40 completely inside said projection of said second conducting structure (scanning/gate line 44) onto said substrate 40; and a fourth conducting structure 50, for example a gate, upon said

Art Unit: 2826

substrate 40, with the projection of said fourth conducting structure (gate 50) onto said substrate 40 separated from said projection of said first (signal/data line 43) and said third conducting structure (source 53) onto said substrate 40, said projection of said fourth conducting structure (gate 50) onto said substrate 40 completely inside said projection of said second conducting structure (scanning/gate line 44) onto said substrate 40; and said projection of said fourth conducting structure (gate 50) onto said substrate 40 approximately parallel to said projection of said third conducting structure (source 53) onto said substrate 40. Note figures 4a, 4c, and column 4 lines 15-36 and 54-68 of Morozumi. Morozumi does not disclose that said side (hereinafter "first side") of said projection of said fourth conducting structure (gate 50) onto said substrate 40 approximately parallel to said projection of said third conducting structure (source 53) onto said substrate 40 is at least seven times longer than said side (hereinafter "second side") of said projection of said fourth conducting structure (gate 50) onto said substrate 40 approximately parallel to said projection of said first conducting structure (signal/data line 43) onto said substrate 40. In his text, Morozumi neglects to compare said first side to said second side. In Morozumi's drawings (which are not to scale) the first side overlaps the second side and for this reason is clearly a little longer. However, given that the drawings are not to scale, there is no guarantee that the first side is disclosed as being seven times longer than the second side, as is required by claim 10.

2. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably

Art Unit: 2826

accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thomas L Dickey whose telephone number is 571-272-1913. The examiner can normally be reached on Monday-Thursday 8-6.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Nathan J Flynn can be reached on 703-308-6601. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

TLD
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